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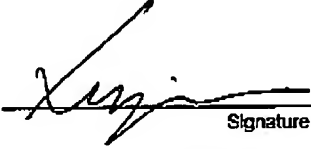
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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional) TSM03-0763	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on <u>December 6, 2006</u> Signature _____ Typed or printed name <u>Judy A. Betts</u>		Application Number <u>10/828,884</u>	Filed <u>April 21, 2004</u>
		First Named Inventor <u>Tseng</u>	
		Art Unit <u>2823</u>	Examiner <u>Khiem D. Nguyen</u>
Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.			
This request is being filed with a notice of appeal.			
The review is requested for the reason(s) stated on the attached sheet(s). Note: No more than five (5) pages may be provided.			
I am the			
<input type="checkbox"/> applicant/inventor.		Signature	
<input type="checkbox"/> assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)		Typed or printed name	
<input checked="" type="checkbox"/> attorney or agent of record. <u>58,322</u>		<u>(972) 732-1001</u>	
Registration number		Telephone number	
<input type="checkbox"/> attorney or agent acting under 37 CFR 1.34.		<u>December 6, 2006</u>	
Registration number if acting under 37 CFR 1.34 _____		Date	
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.			
<input checked="" type="checkbox"/> Total of <u>1</u> forms are submitted.			

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.8. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Tseng	Docket No.:	TSM03-0763
Serial No:	10/828,884	Art Unit:	2823
Date Filed:	April 21, 2004	Examiner:	Khiem D. Nguyen
Title:	Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process		

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRE-APPEAL BRIEF REQUEST FOR REVIEW

Dear Sir:

Claims 1-19 and 31-40 are pending in this application. Applicant appeals the final rejection of claims 1-4, 9, 12, 14, 15, 18, 19, 31-37 and 40, which were rejected as being obvious over the combination of Adan (U.S. Pub. 2003/0136992) in view of Yeo (U.S. Patent 6,521,939 to Yeo et al.).

Claim 1 was rejected under 35 U.S.C. 103(a) as being unpatentable over Adan in view of Yeo. Claim 1 relates to forming a varactor and recites "forming first, second and third N+ regions in the same N well." Applicant respectfully submits that the combination with Yeo, as proposed in the final office action and in the Advisory Action, would render Adan unsatisfactory for its intended purpose.

1. Issue

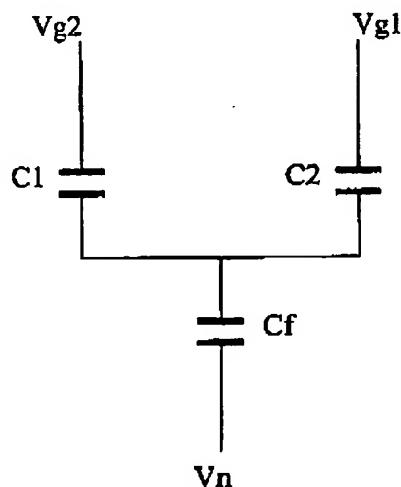
The issue in the application is whether or not it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the forming of Adan varactor

structure to include forming the first 36, second 37 and third 24 N+ doped region of Adan in the same N well in view of Yeo.

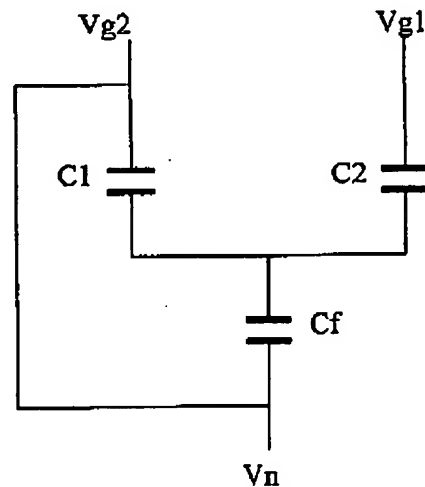
2. Discussion

Each pending independent claim recites first, second, and third varactor elements comprising doped regions formed in the same N well. Examiner has conceded that Adan does not teach forming all the N+ doped regions in the same N well. Examiner has asserted that it would have been obvious to combine the teaching of Adan and Yeo to form the first 36, second 37 and third 24 N+ doped regions of Adan (FIG. 5) in the same N well of Yeo.

In response, the Applicant has shown that the proposed modification to Adan is without merit because, by combining Adan and Yeo, and forming the first 36, second 37 and third 24 N+ doped regions in the same N well, it simply shorts control terminal Vg2 and control terminal Vn (FIG. 5) and destroys the intended purpose of Adan as a multi-terminal MOS varactor (page 2, paragraph [0033] and page 3, paragraph [0051]). This is illustrated below:



(This is the schematic of Adan when the doped regions are in separate wells – as taught by Adan)



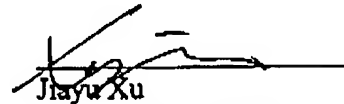
(This is the resulting schematic if the doped regions of Adan are in the same well as taught by Yeo; note the capacitors C1 and Cf are short-circuited)

In light of the above, the proposed combination in the Final Office Action will render Adan non-operative for its intended purpose and, therefore, does not support a *prima facie* basis of obvious rejection. As stated in the MPEP, "If proposed modification would render the prior art invention being modified unsatisfactory for its intended purpose, then there is no suggestion or motivation to make the proposed modification (page 2100-131, Rev 2, May 2004)."

Unfortunately, the Advisory Action failed in response to this point and reiterated the rejection in the Final Office Action. Applicant respectfully requests the panel reconsider this argument and the patentability of the claims.

Respectfully submitted,

December 6, 2006


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